

ABSTRACT OF THE DISCLOSURE

A microphone unit capable of suppressing the sensitivity reduction due to a parasitic capacitance that occurs depending on the structure of an electret capacitor, can 5 be realized by the following manner. Specifically, an output signal (Vout) that is the inverted output of an input signal (Vin) is inputted to an operational amplifier (OP2) that is an inverting amplifier, such that the output signal (Vout) has the same phase as the input signal (Vin) and is amplified. With an output signal (Vfb) of the operational amplifier (OP2) connected to a first electrode of a parasitic capacitor (CX), the parasitic 10 capacitor (CX) functions as a coupling capacitor, while a feedback is applied to an electret capacitor (EC). This allows for an increase in the voltage between both terminals of the electret capacitor (EC), and thus suppresses that the sensitivity of the microphone unit is lowered due to the parasitic capacitor.

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